Attorney Docket No. 10559-177001 Serial No. 09/771,374 Amendment dated July 21, 2004 Reply to Office Action dated April 21, 2004

Amendments to the Specification:

Please replace the paragraph beginning at page 8, line 24 with the following amended paragraph:

Four processes, each requiring access to a shared computer resource, such as a SDRAM bank are indicated as "A", "B", "C" and "D", and the access value being represented as "a" for each process. It is to be understood that in this illustrative example each [[f]] of the four processes, "A", "B", "C" and "D" have priority values representing "high" priority. is initialized such that Process A is provided access to the shared computer resource, followed by Process B, then Process C and finally Process D. In this example, the access values for Process A, B, C and D are in a proportional ratio respectively having values of 1:1:4:2. Therefore, Process A should receive a maximum of 12.5% of available bandwidth, Process B a maximum of 12.5%, Process C a maximum of 50% and Process D a maximum of 25% of available bandwidth. In the following Table, the status of the access value of each of the processes, A, B, C and D are indicated per unit interval of time, here indicated in 'clock cycles'. In the following Table, the access values for each process are represented by " α_x " where "x" is the process A, B, C or D. In this particular example each request for a service,



Attorney Docket No. 10559-177001 Serial No. 09/771,374 Amendment dated July 21, 2004 Reply to Office Action dated April 21, 2004

such as access to a shared computer resource, requires four clock cycles to satisfy the request.